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Prior art  
FJ  
6-9-88*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Masao TAGUCHI

Serial Number: 016,611

Group Art Unit: 253

Filed: February 19, 1987

Examiner: J. Clawson, Jr.

For: DYNAMIC RANDOM ACCESS MEMORY HAVING TRENCH CAPACITOR

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner  
of Patents and Trademarks  
Washington, D. C. 20231

June 1, 1988

Sir:

In compliance with 37 C.F.R. 1.56, the applicant, by the undersigned attorney, hereby notifies the Examiner of the existence of the following additional copending commonly assigned sister United States patent application and to the publications cited in the European Search Report issued in the counterpart of this application.

- (1) U.S. Patent application S.N. 020,983  
By: Masao TAGUCHI  
Filing Date: March 2, 1987  
For: Dynamic Random Access Memory Having Trench Capacitor.
- (2) European Patent application No. 0 171 131  
By: MORI et al  
Filing Date: March 19, 1985  
For: Semiconductor Device with MIS Capacitor and Method of Manufacturing the Same.
- (3) European Patent application No. 0 149 799  
By: UCHIDA  
Filing Date: December 14, 1984  
For: Semiconductor Memory Device
- (4) Japanese Publication No. 59-141262
- (5) Japanese Publication No. 60-54472

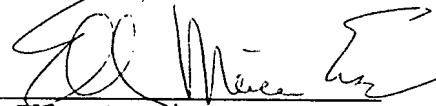
Copies of references (2), (3), (4) and (5) of the listed material information mentioned above are attached hereto along with form PTO-FB-A820.

In the event that any fees are due with this paper, please charge our Deposit Account No. 01-2395.

Respectfully submitted,

ARMSTRONG, NIKAIDO, MARMELSTEIN & KUBOVCIK

By:

  
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Atty. Case No.  
P21-698-AM87145

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Enclosures: Form PTO-FB-A820 and (4) References.